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APPLICATION NO. FILING DATE FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. MICT-0012-D1-US 09/853,111 05/10/2001 7172 Salman Akram (97-0141) 21906 7590 09/10/2003 TROP PRUNER & HU, PC **EXAMINER** 8554 KATY FREEWAY CHAMBLISS, ALONZO SUITE 100 HOUSTON, TX 77024 PAPER NUMBER

ART UNIT

2827

DATE MAILED: 09/10/2003

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action	Application No.	Applicant(s)	
	09/853,111	SALMAN AKRAM	
	Examiner	Art Unit	
	Alonzo Chambliss	2827	
The MAILING DATE of this communication appears on the cover sheet with the c rrespondence address			
THE REPLY FILED FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.			
PERIOD FOR REPLY [check either a) or b)]			
a) The period for reply expiresmonths from the mailing date of the final rejection. b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f). Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).			
1. A Notice of Appeal was filed on Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.			
2. The proposed amendment(s) will not be entered because:			
(a) they raise new issues that would require further consideration and/or search (see NOTE below);			
(b) They raise the issue of new matter (see Note below);			
(c) Ithey are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or			
(d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.NOTE:			
3. Applicant's reply has overcome the following rejection(s):			
4. Newly proposed or amended claim(s) would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).			
5. The a) affidavit, b) exhibit, or c) request for reconsideration has been considered but does NOT place the application in condition for allowance because:			
6. The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.			
7. ☐ For purposes of Appeal, the proposed amendment(s) a) ☐ will not be entered or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.			
The status of the claim(s) is (or will be) as follows:			
Claim(s) allowed: Claim(s) objected to:			
Claim(s) rejected: <u>15-22 and 32-35</u> .			
Claim(s) withdrawn from consideration:			
8. The proposed drawing correction filed on is a) approved or b) disapproved by the Examiner.			
9. Note the attached Information Disclosure Statement(s)(PTO-1449) Paper No(s)			
10.⊠ Other: <u>See Continuation Sheet</u>			
		Alonzo Chambliss Patent Examiner Art Unit: 2827	mliles

U.S. Patent and Trademark Office PTOL-303 (Rev. 04-01) Continuation of 10. Other: Sota teaches one of the dies is mounted on top of the leadframe fingers (i.e. the top die 1) and the other (i.e. the lower die 1) is mounted on the die by die pad 2 on the leadframe fingers (see Figs. 3b-3e). Ball discloses a first and second semiconductor dies mounted in a back to back configuration. In Fig. 3, die 102 is electrically connected to leadframe 120 by wire bonding and flip-chip connection. Thus, allowing the die 102 to transmit signals faster to the leadframe by having two types of electrical connection between the die and the leadframe. Therefore, one skilled in the art at the time of the invention would readily recognize increasing the transmission of signals by using a wire connection between die 104 and lead finger 118 of leadframe 120, since die 102 has established successful electrical of increasing signals from the die to the leadframe using a flip-chip connection and wire bonding connection. Furthermore, claim 33 does not further limit claim 32, since the additional language inserted into claim 32 is recited in claim 33.